

DOCKET NO. TI 37691

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Husam N. Alshareef, et al.

Serial No.: N/A

Filed: Herewith

For: A SEMICONDUCTOR DEVICE HAVING MULTIPLE WORK
FUNCTIONS AND METHOD OF MANUFACTURE THEREFOR

Group No.: N/A

Examiner: N/A

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

U.S. Patent No.

Inventor

Date

6,660,577 B2

Chen et al.

December 9, 2003

References:


Qiang Lu, et al.; "METAL GATE WORK FUNCTION ADJUSTMENT FOR FUTURE CMOS TECHNOLOGY"; 2001 Symposium on VLSI Technology Digest of Technical Papers; 2001 IEEE.

Pushkar Ranade, et al.; "TUNABLE WORK FUNCTION MOLYBDENUM GATE TECHNOLOGY FOR FDSOI-CMOS"; IEDM, 2002 IEEE; Pgs 363-366.

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Respectfully submitted,

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Substitute for form 1449/PTO

(Use as many sheets as necessary)

Complete if Known

Application Number	TI 37961
Filing Date	Herewith
First Named Inventor	Husam N. Alshareef
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	TI 37691

Sheet	1	of	2
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[illegible][illegible]

**Examiner
Signature**

Date	
Considered	

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	TI 37961
				Filing Date	Herewith
				First Named Inventor	Husam N. Alshareef
				Art Unit	N/A
				Examiner Name	N/A
Sheet	2	of	2	Attorney Docket Number	TI 37691

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Qiang Lu, et al.; "METAL GATE WORK FUNCTION ADJUSTMENT FOR FUTURE CMOS TECHNOLOGY"; 2001 Symposium on VLSI Technology Digest of Technical Papers; 2001 IEEE.	
		Pushkar Ranade, et al.; "TUNABLE WORK FUNCTION MOLYBDENUM GATE TECHNOLOGY FOR FDSOI-CMOS"; IEDM, 2002 IEEE; Pgs 363-366.	

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